

FIG.\_2

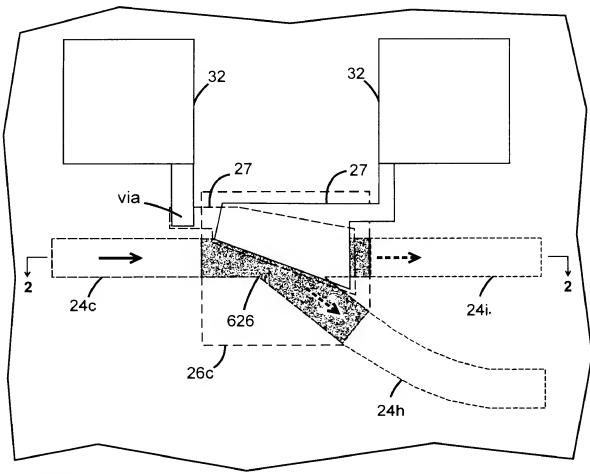


FIG.\_3

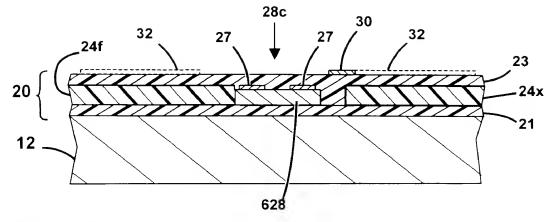


FIG.\_4-1

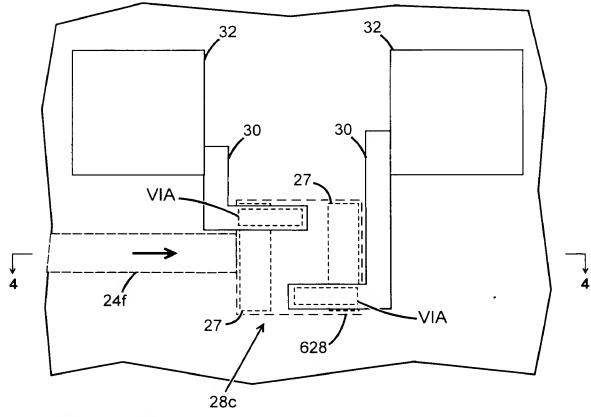
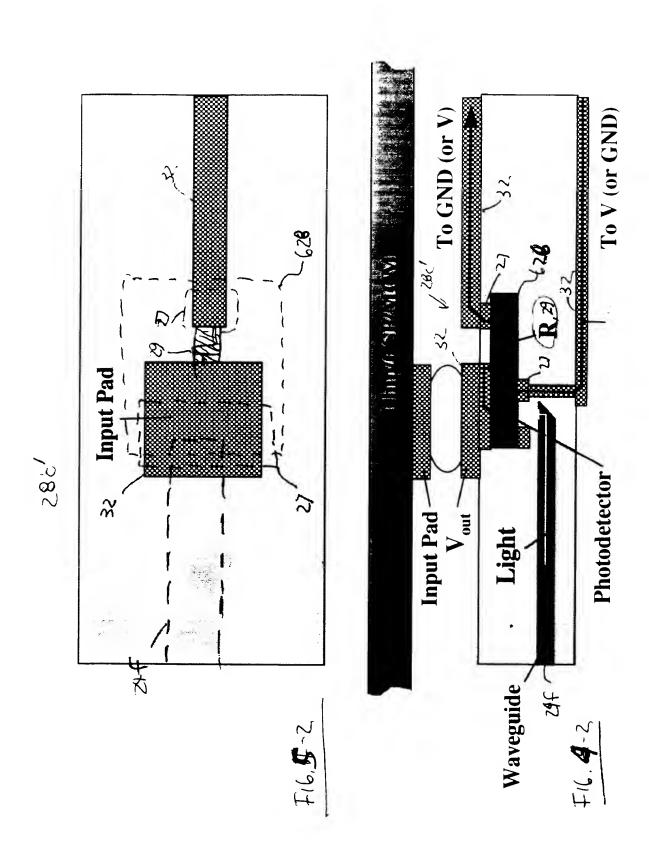
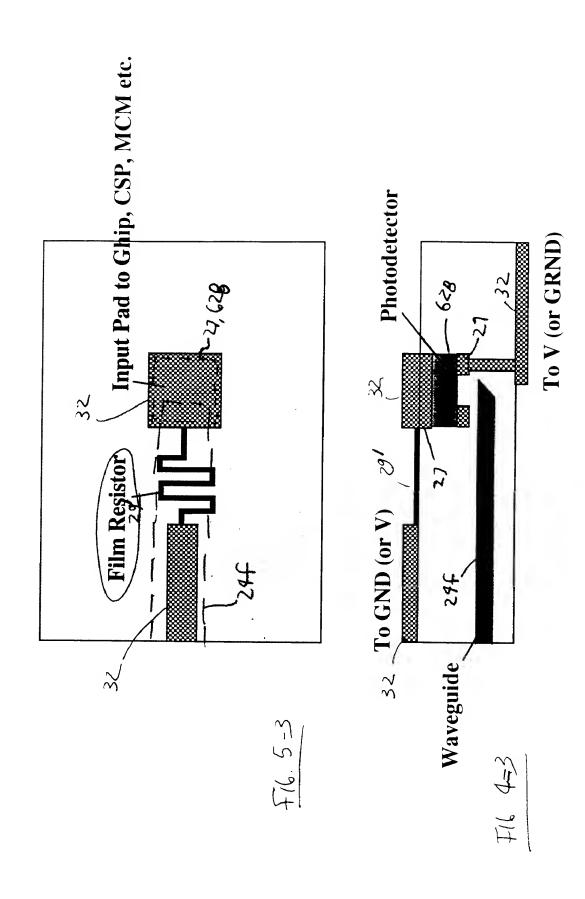
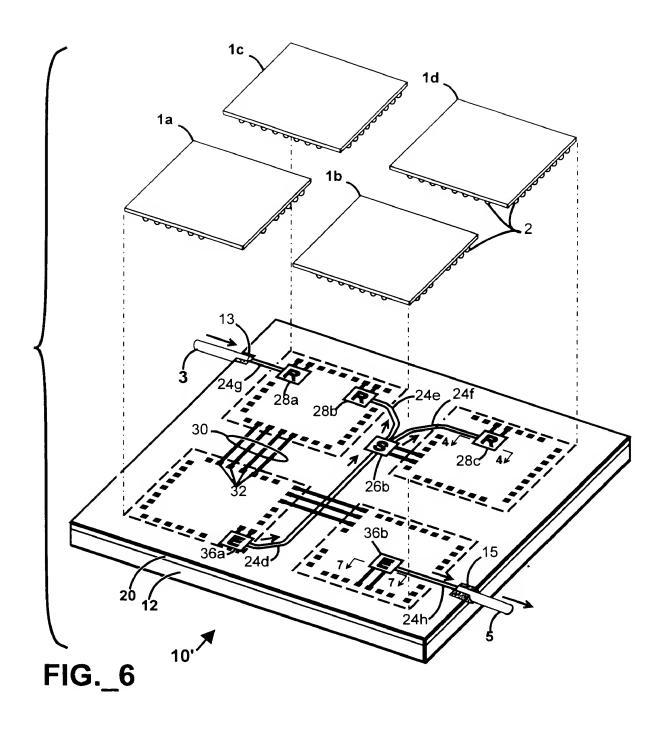


FIG.\_5-1







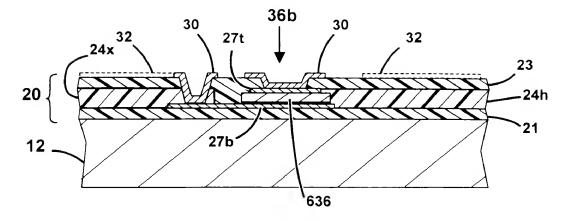


FIG.\_7

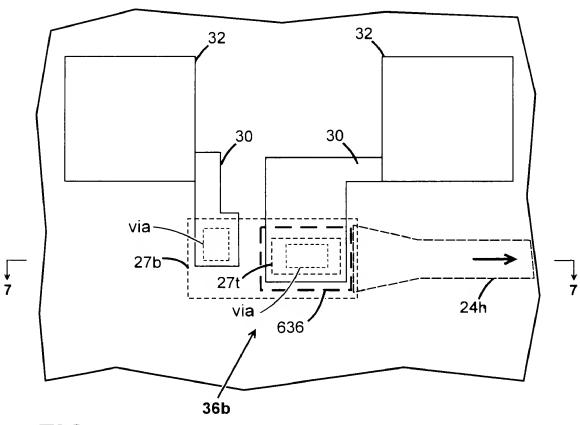
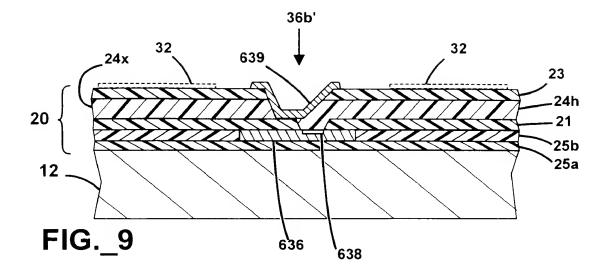


FIG.\_8



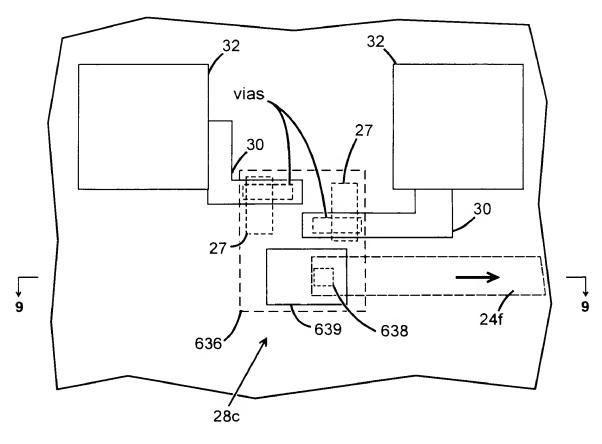
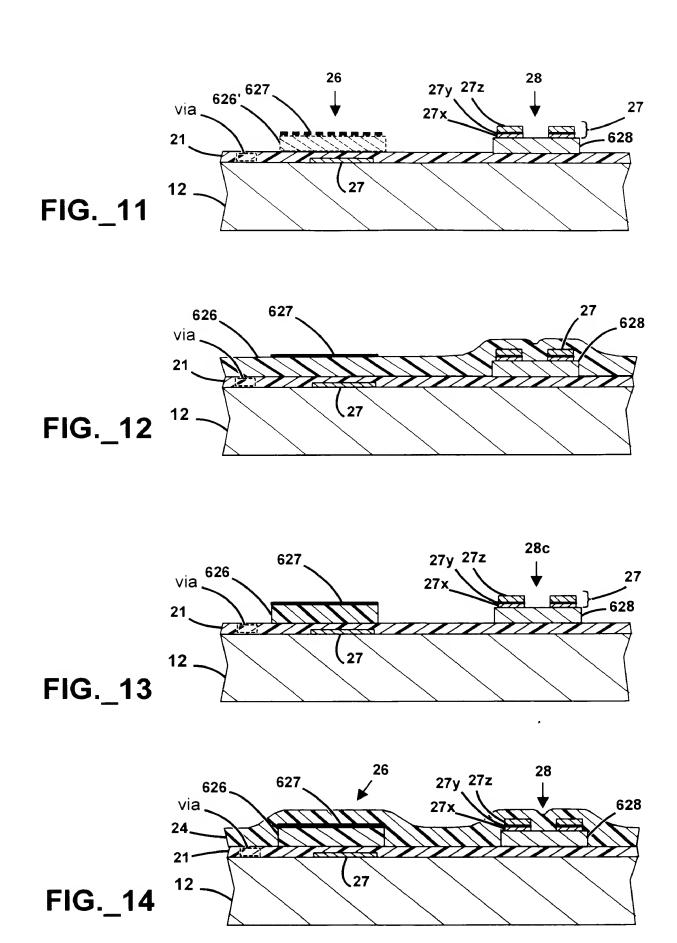
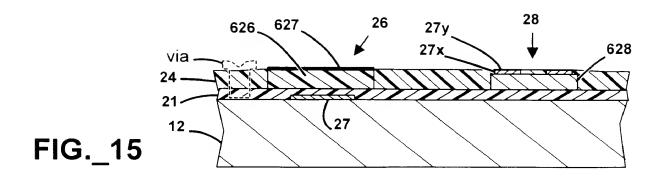
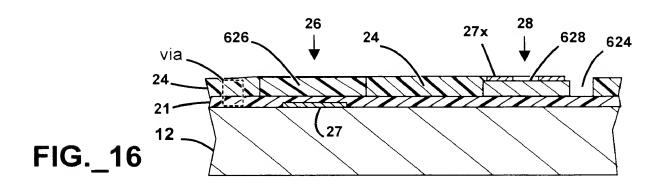
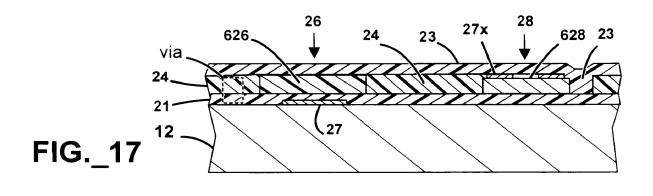


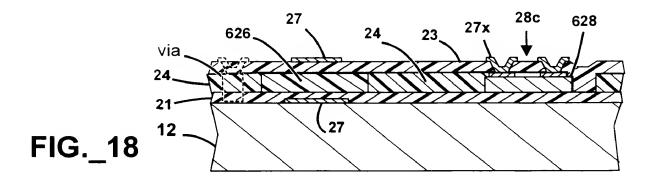
FIG.\_10











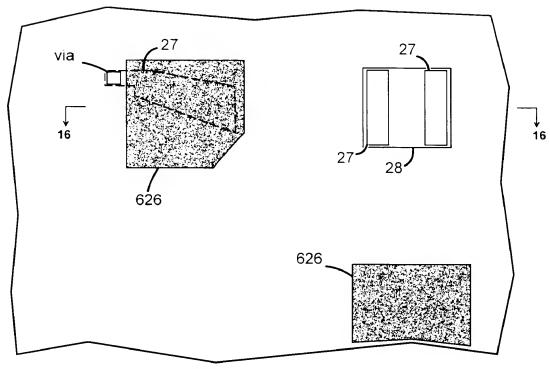
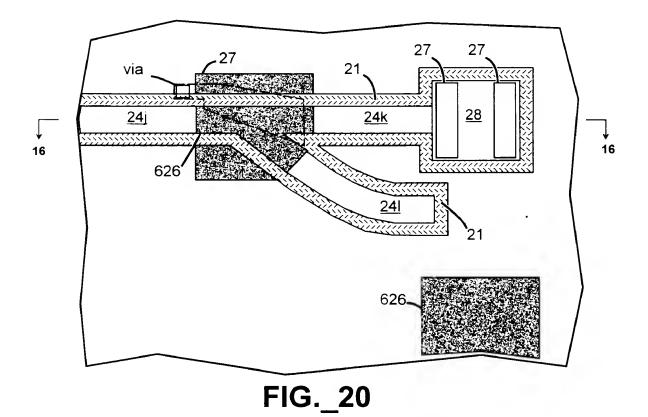


FIG.\_19



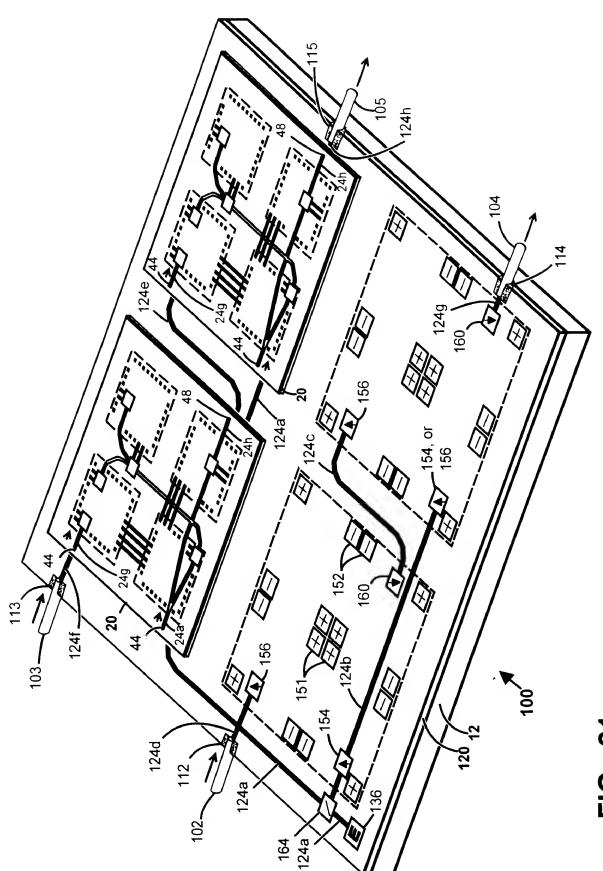


FIG.\_21

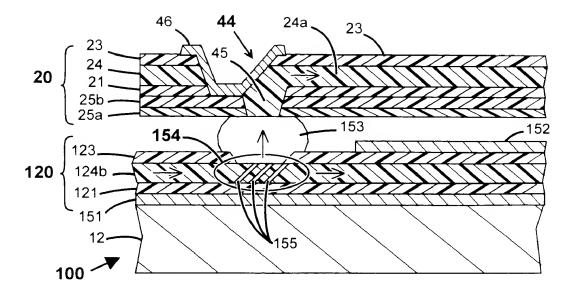


FIG.\_22

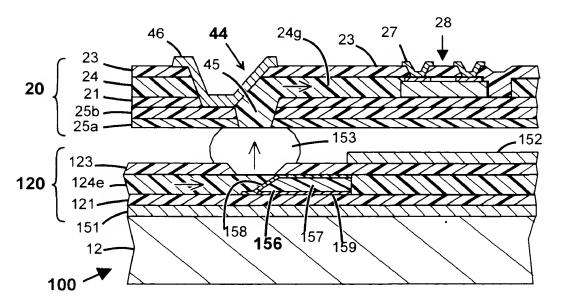


FIG.\_23

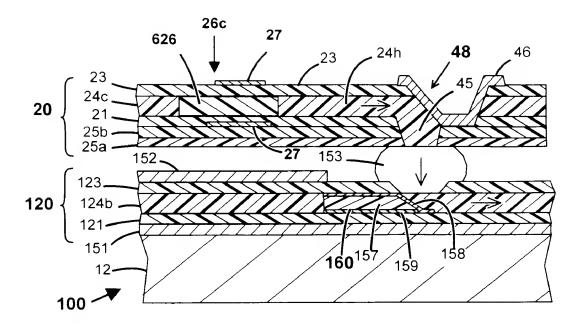
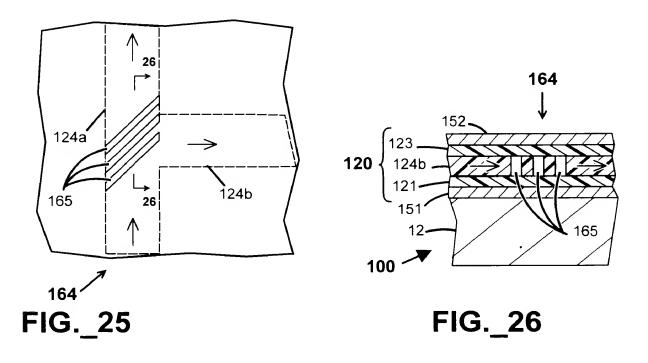
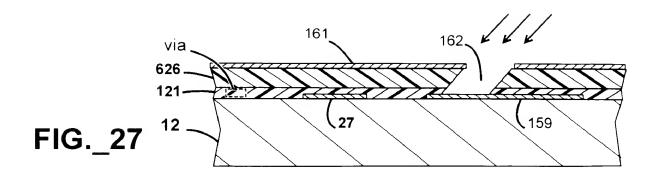
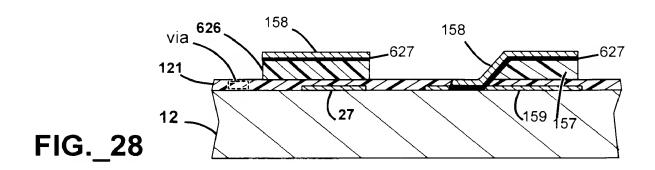
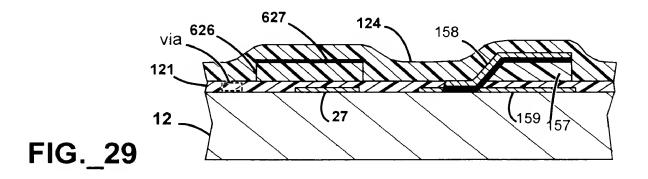


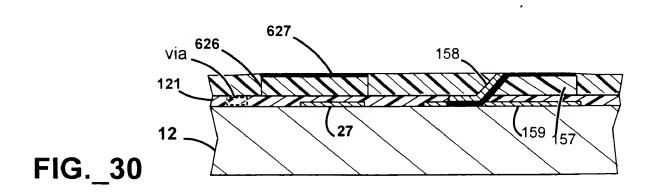
FIG.\_24

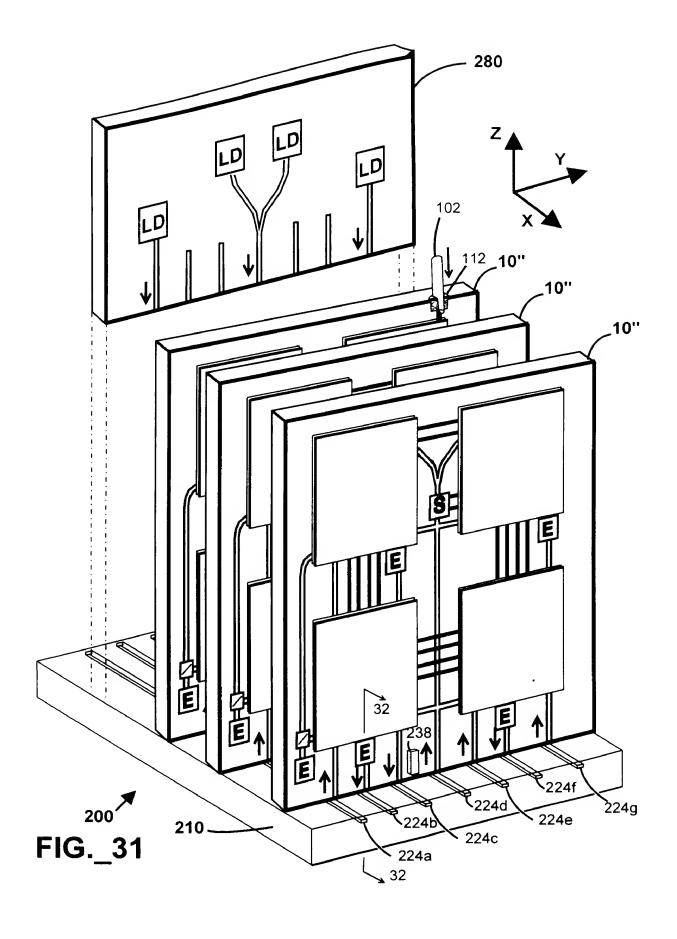


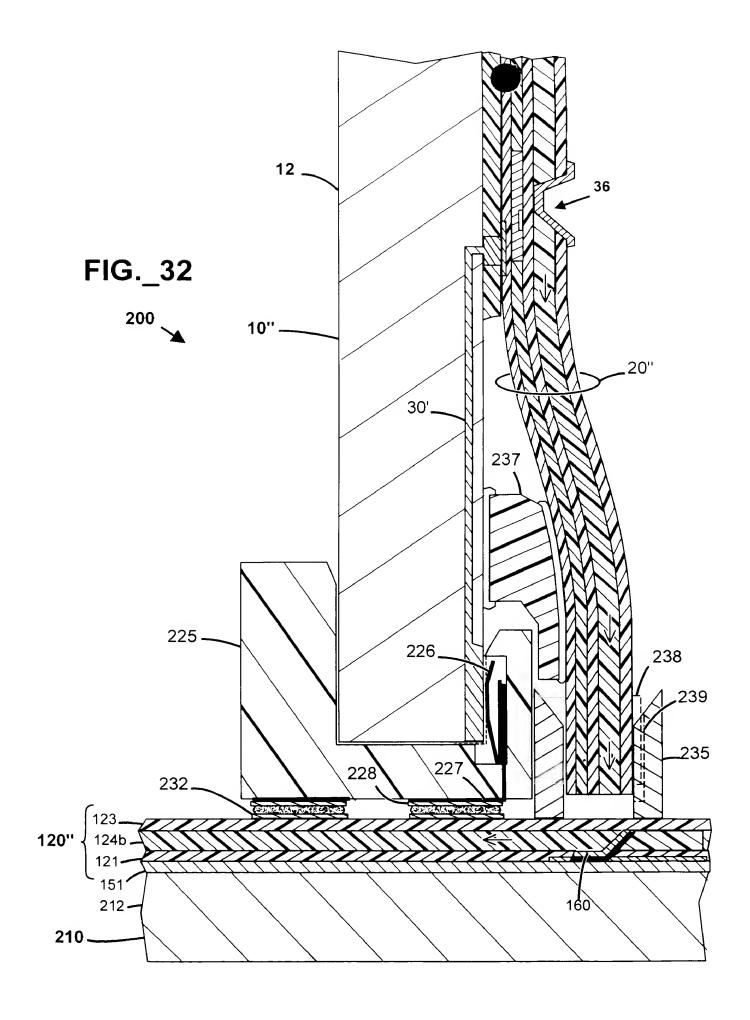


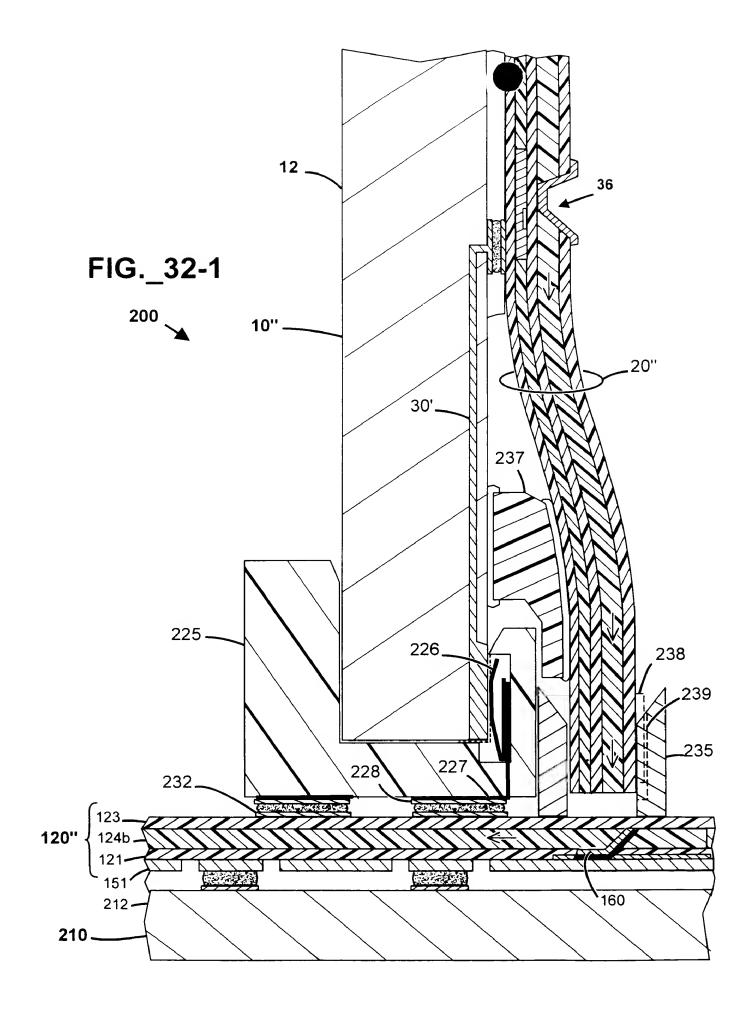


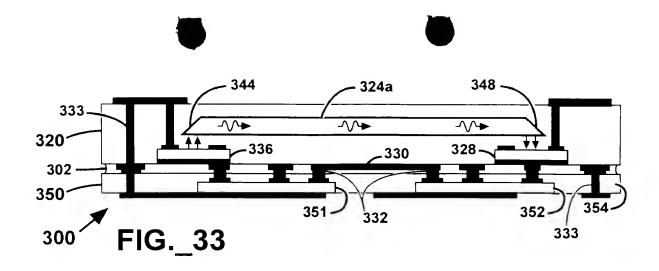


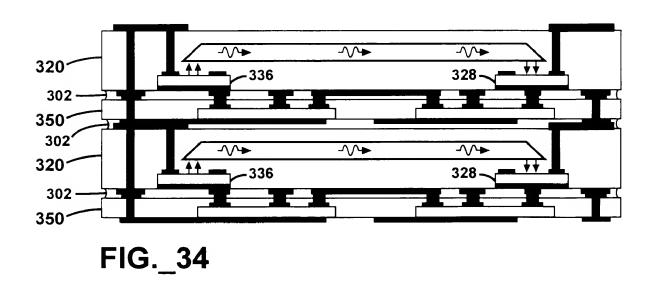


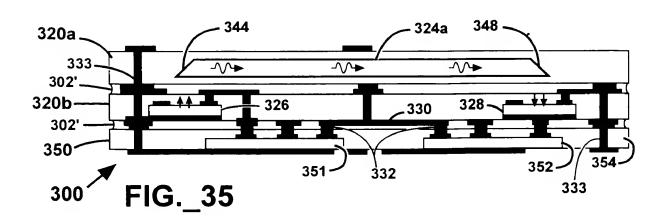












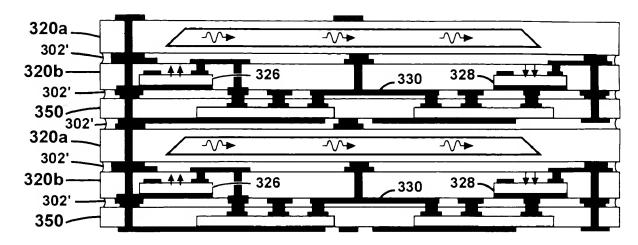
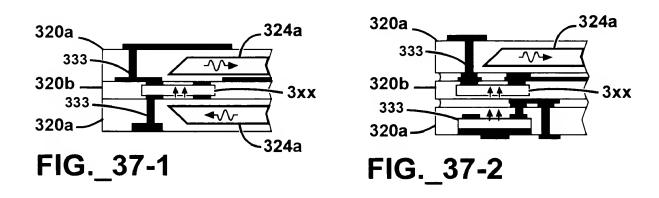
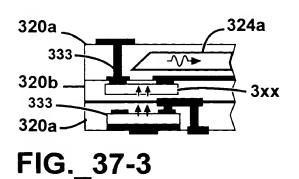
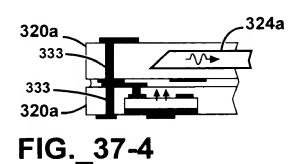


FIG.\_36







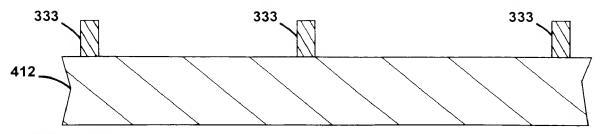


FIG.\_38

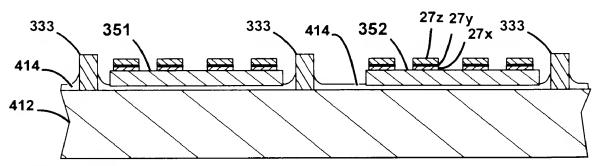


FIG.\_39

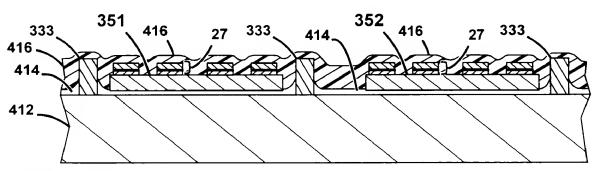


FIG.\_40

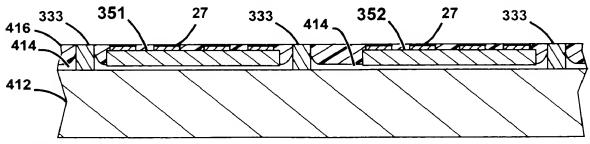


FIG.\_41

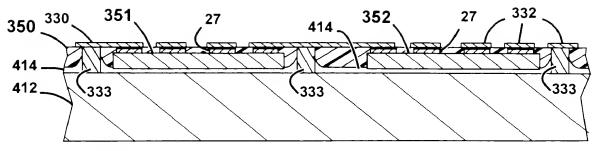


FIG.\_42

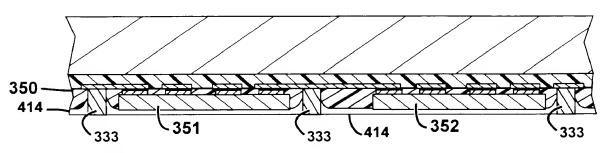


FIG.\_43

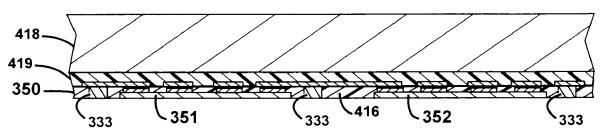


FIG.\_44

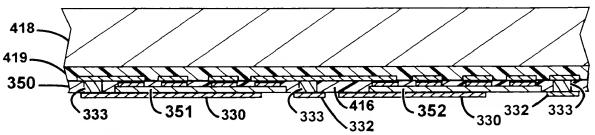
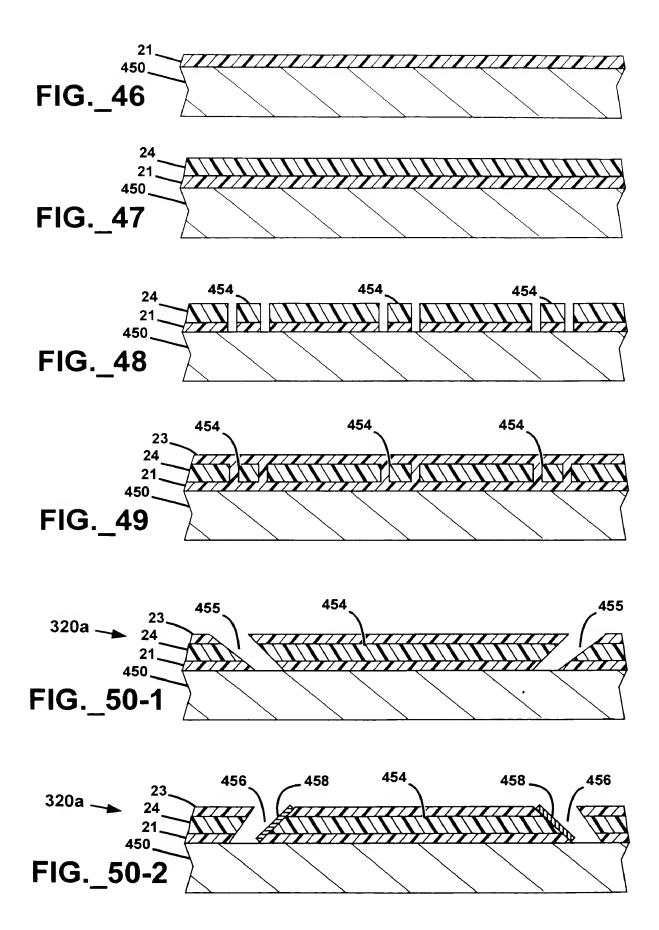
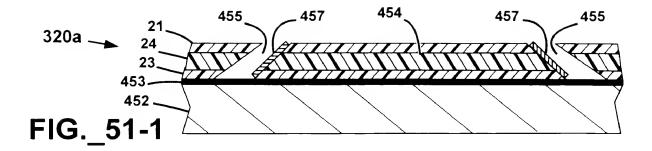
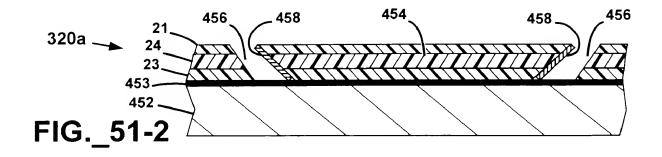
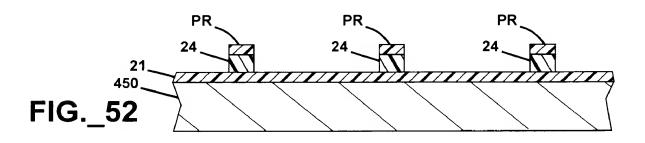


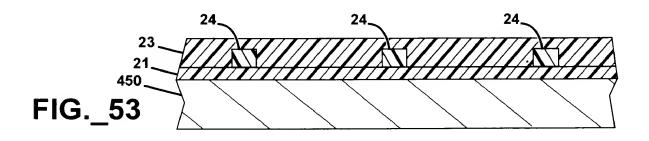
FIG.\_45

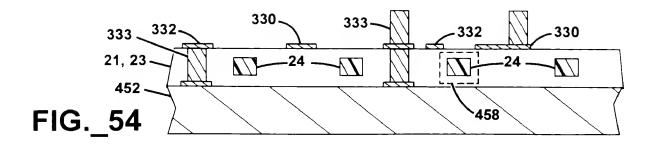


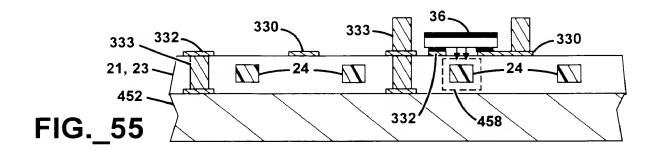


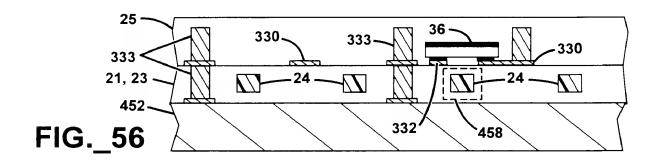


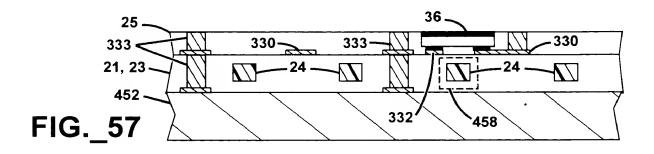


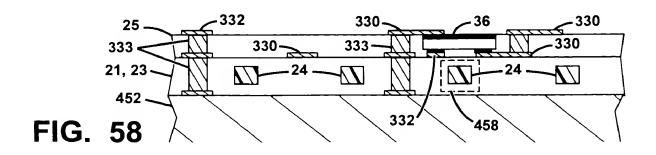


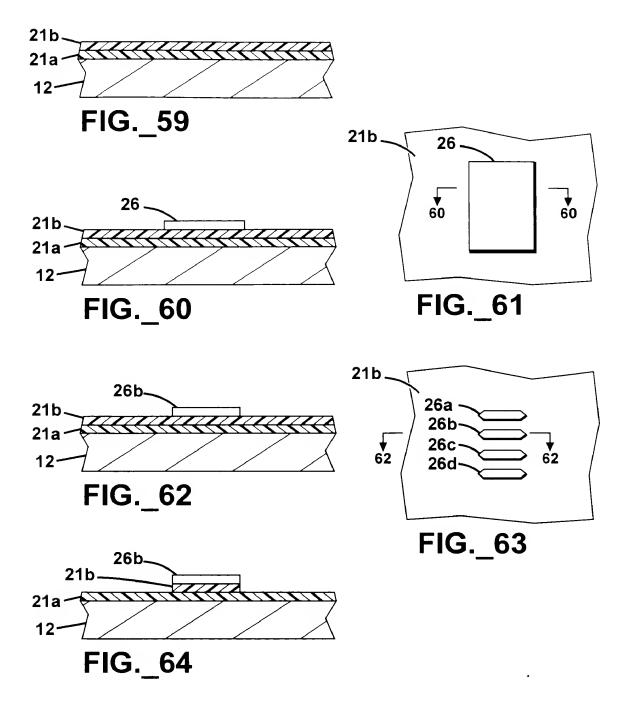


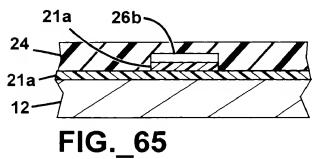


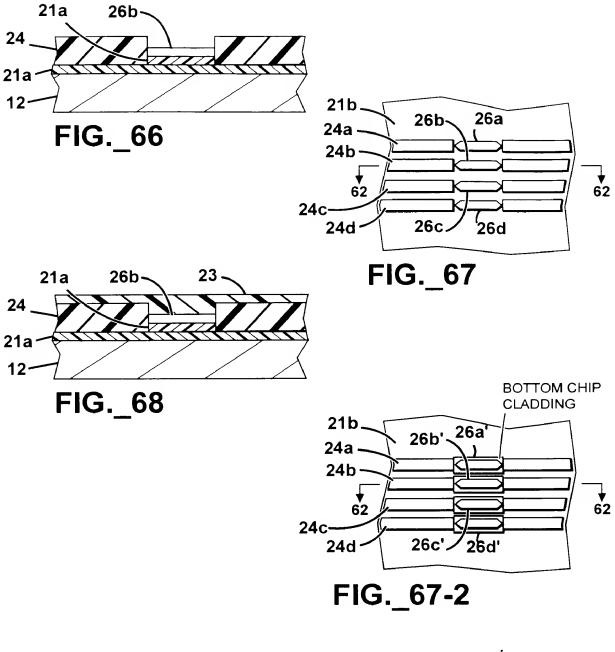


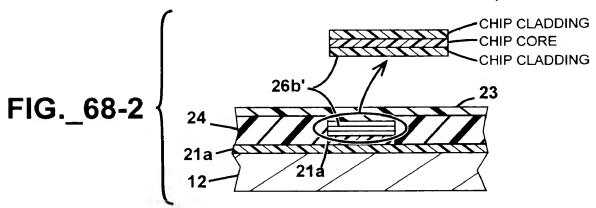


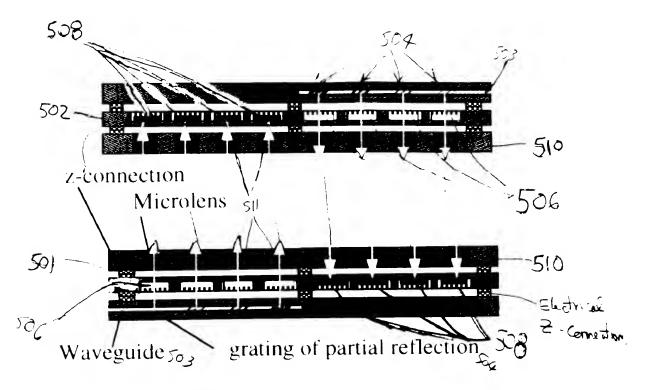




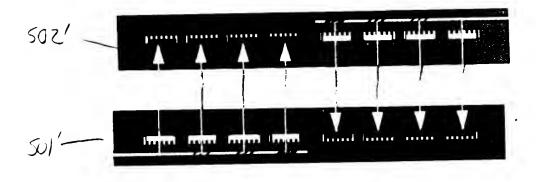




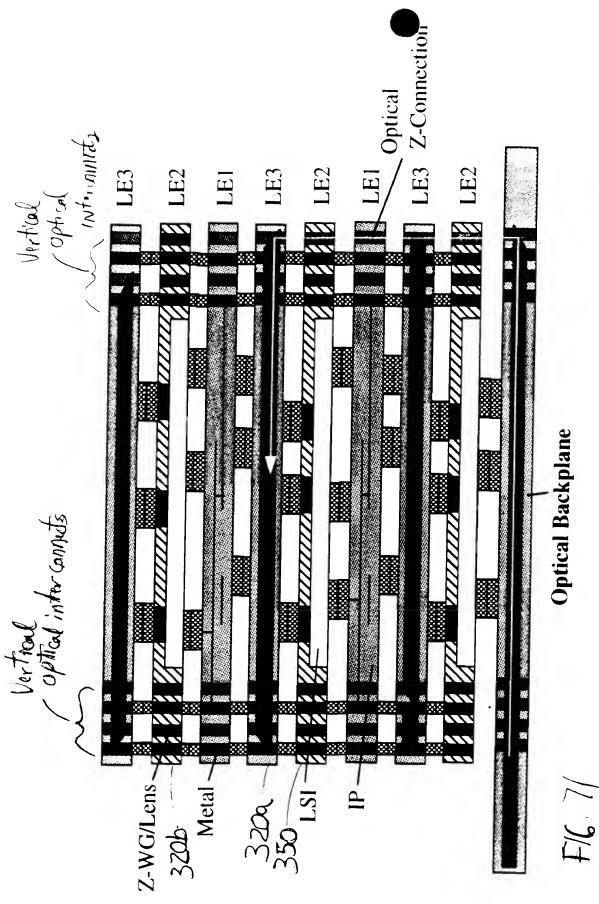


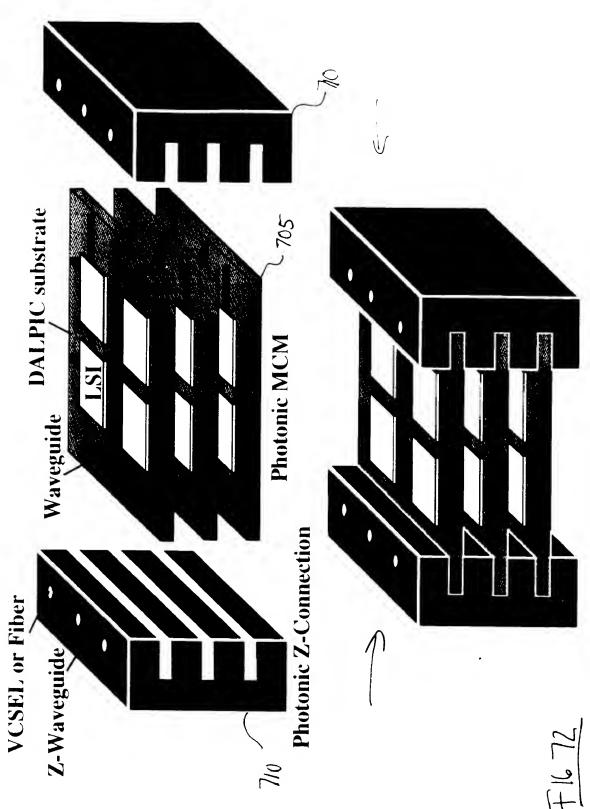


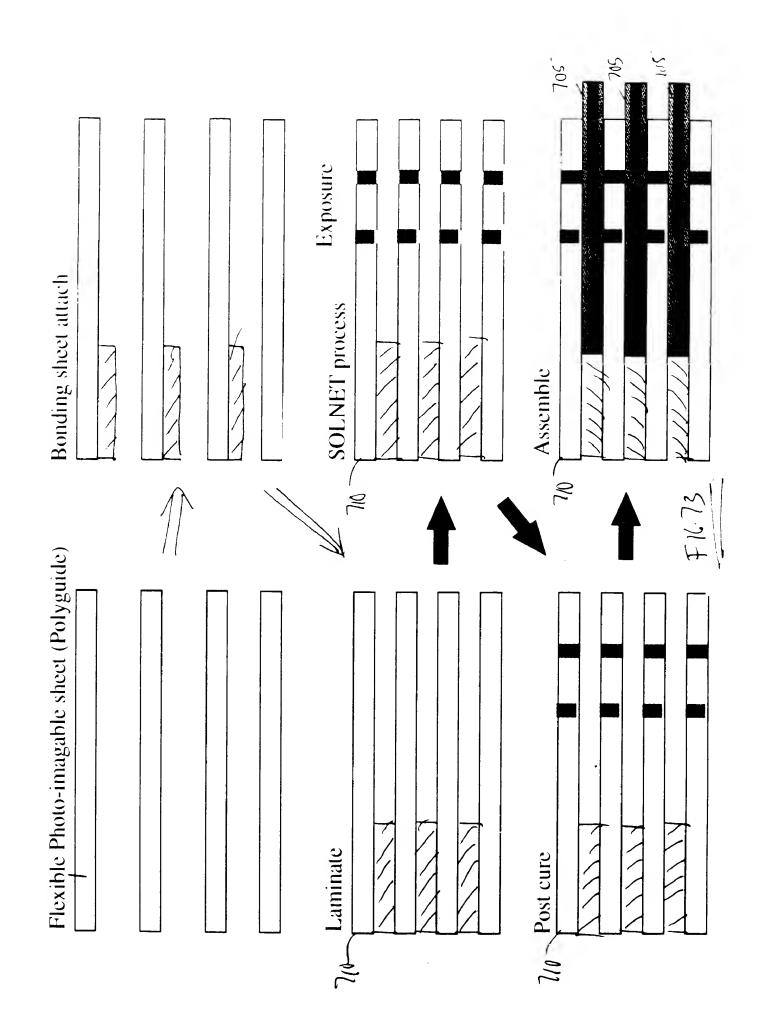
<u>F16.89</u>

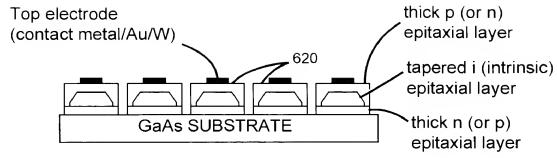


F16.70

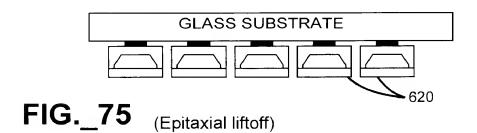


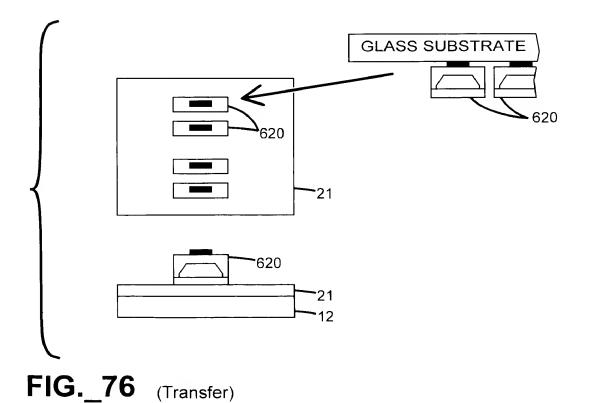






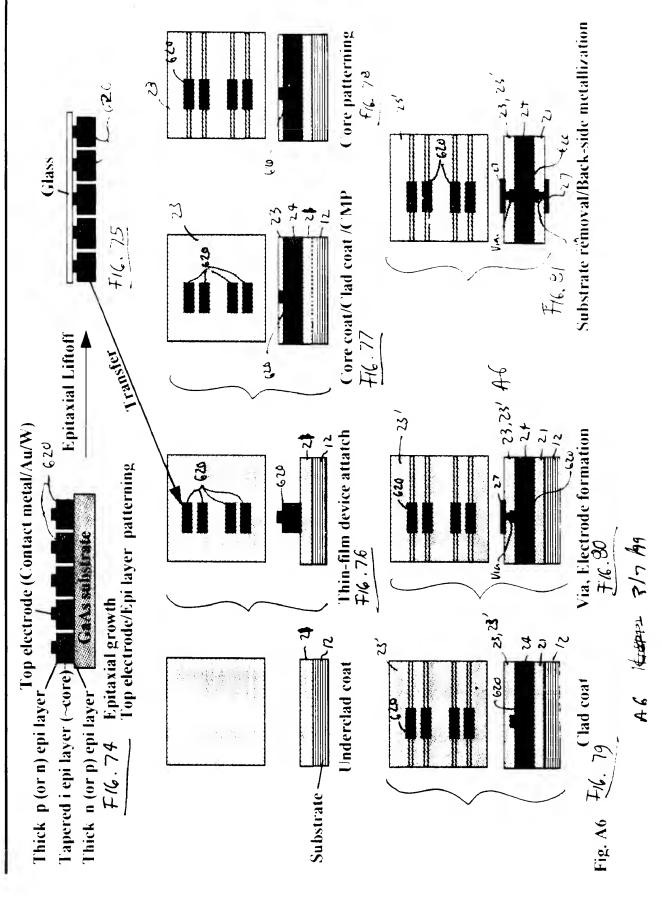
**FIG.\_74** (Epitaxial growth and patterning)

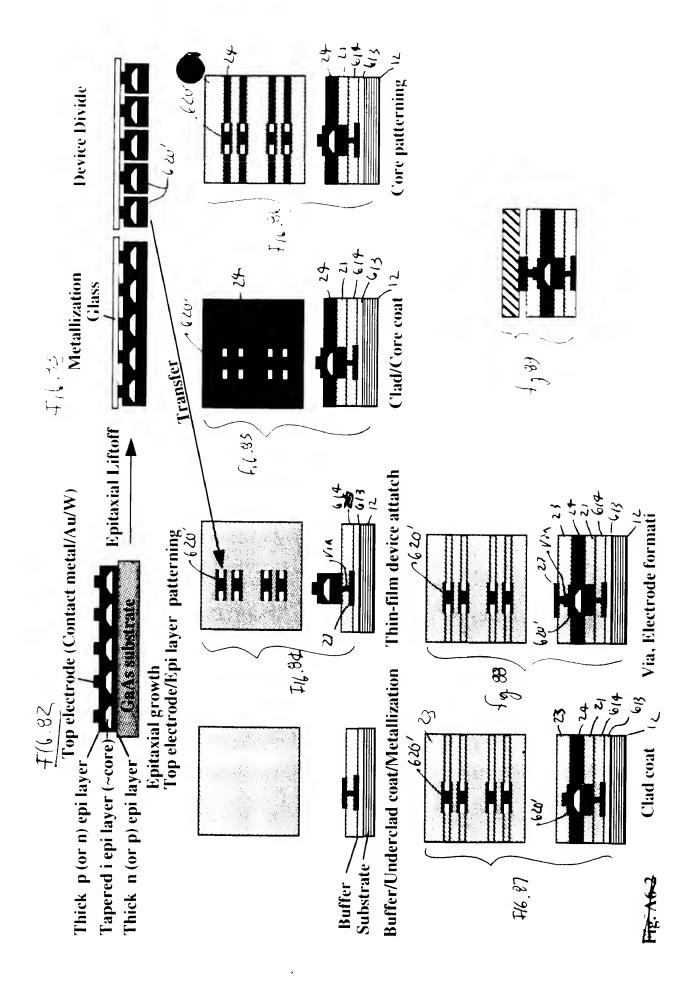


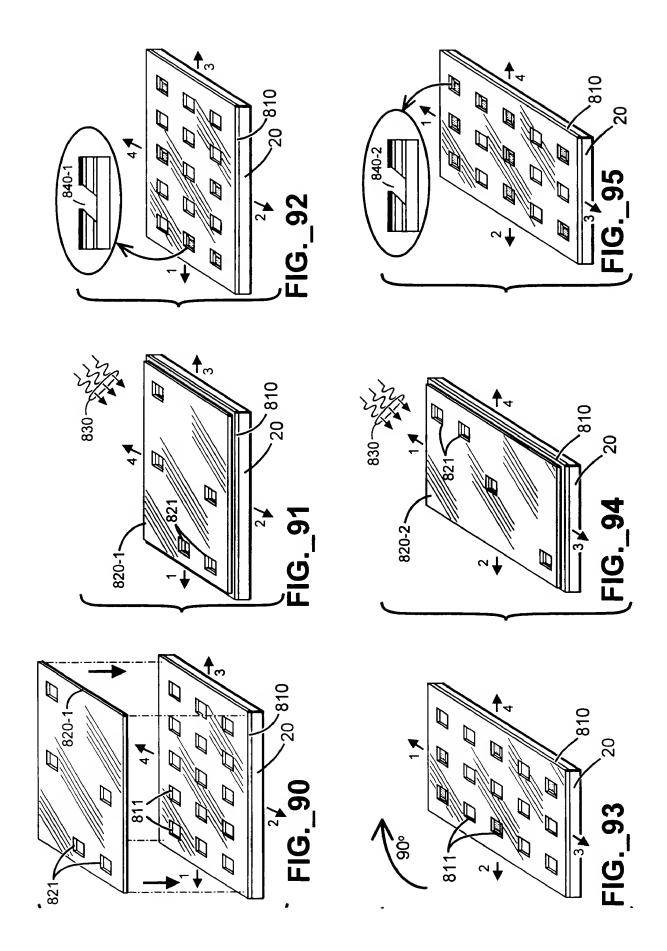


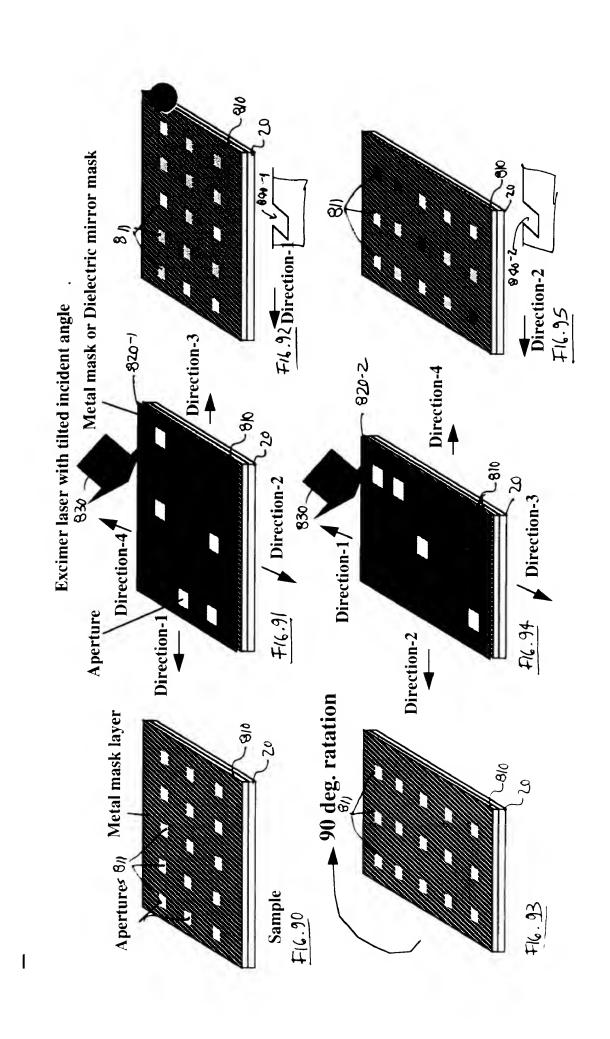
FUNTSU Computer Packaging Technologies, Inc.

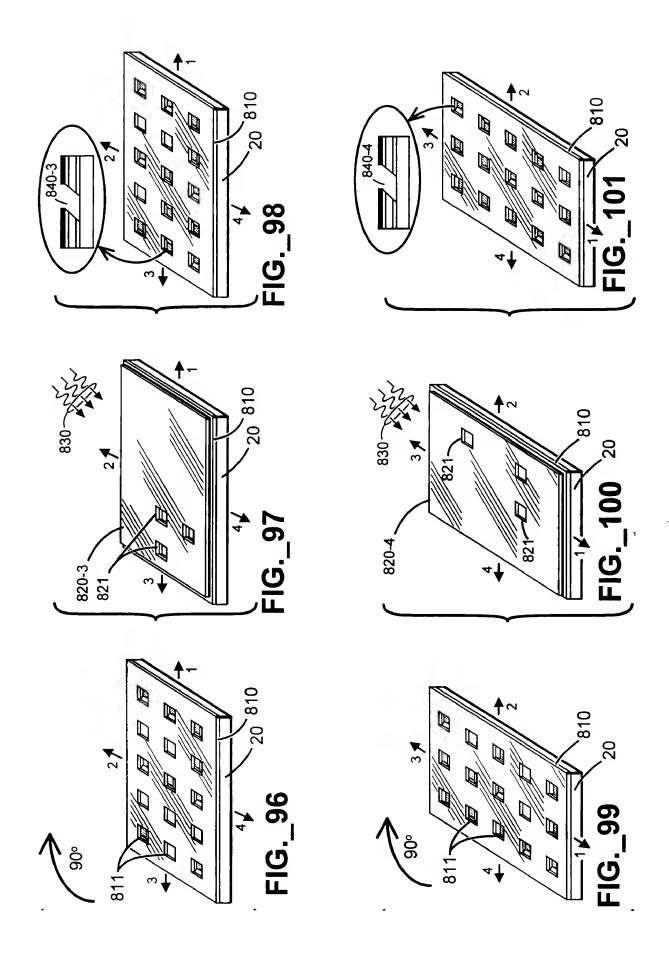
## Example of Light Modulator (or Photodetector) Integration

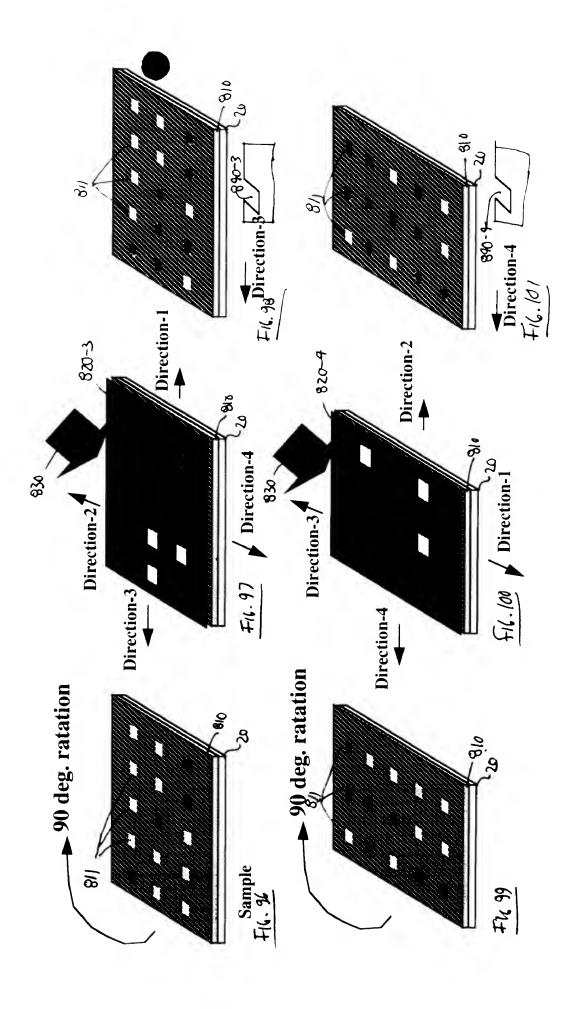


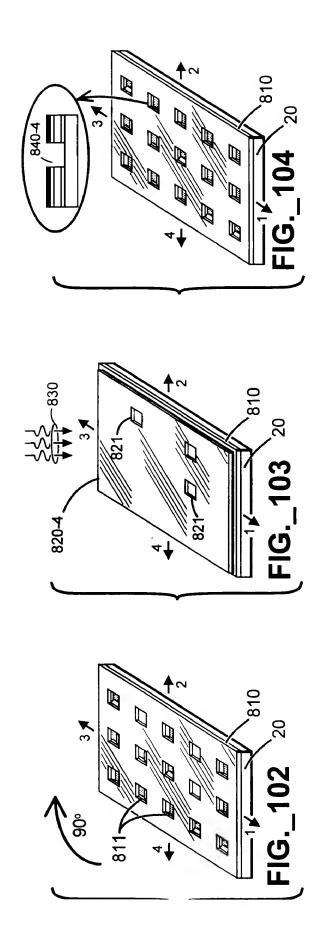


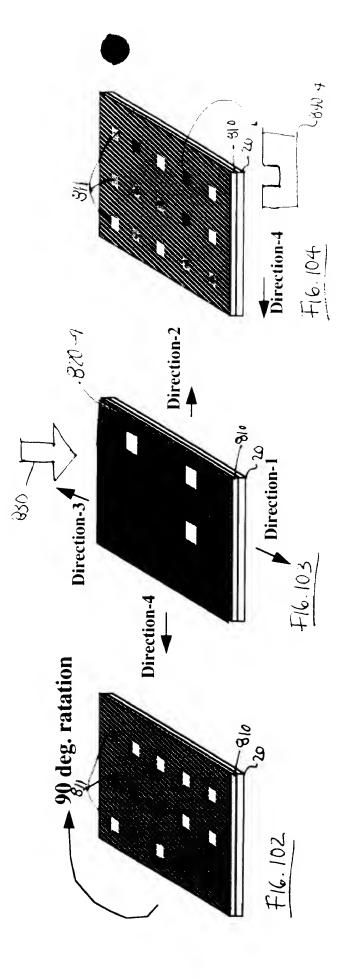












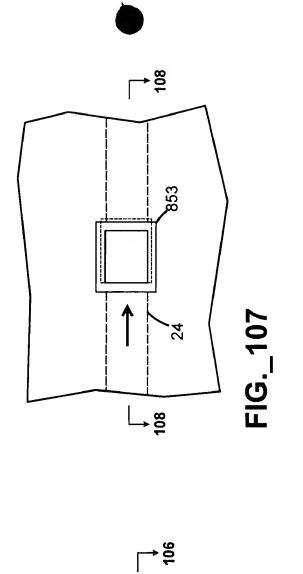
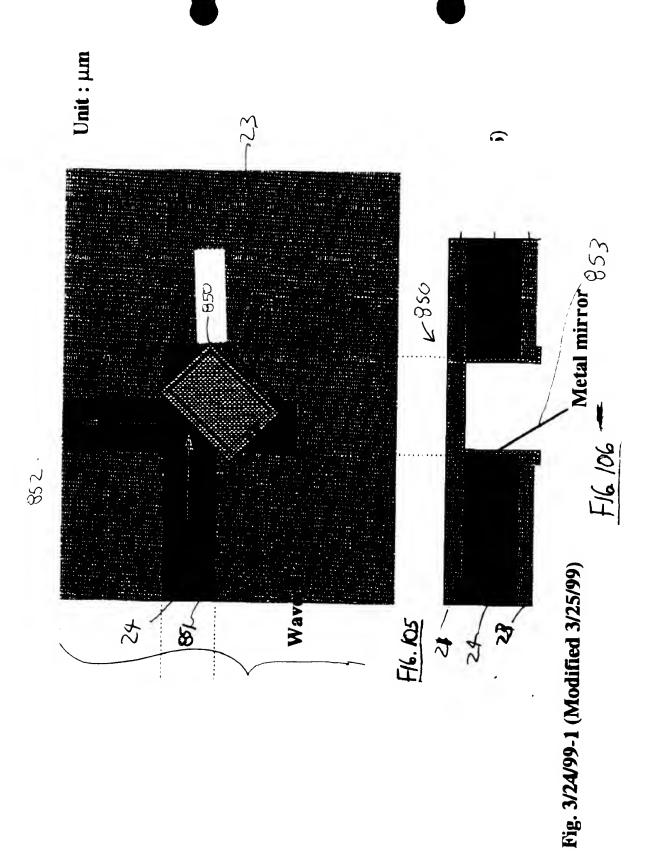


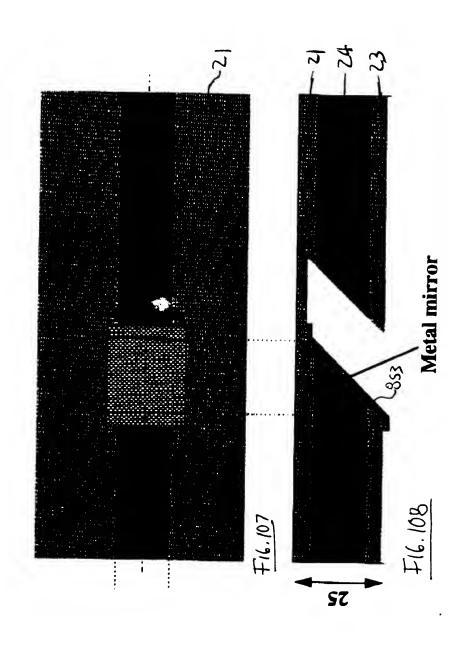
FIG.\_105

FIG.\_106

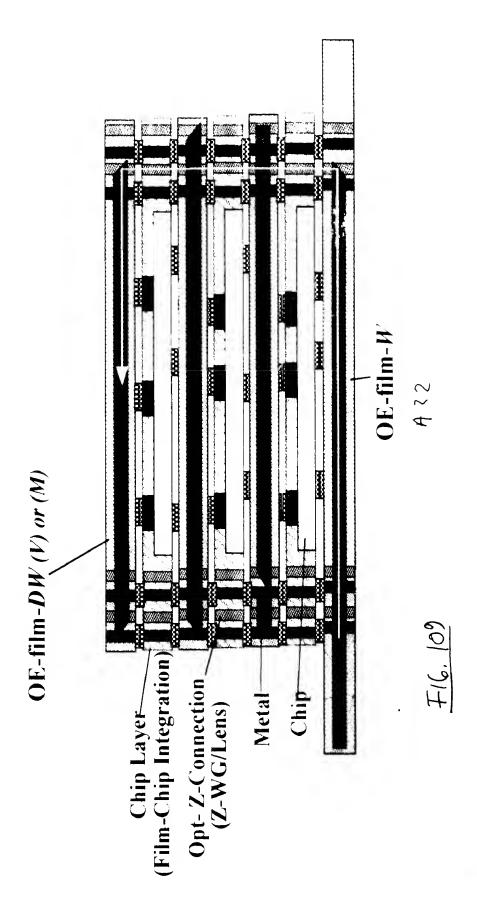
FIG.\_108



cl 3/25/8



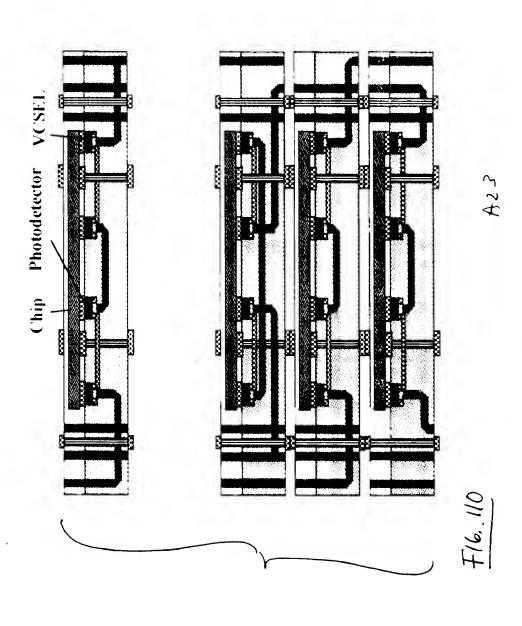
### GS CX/CXX OE Solution --- OE-3D-Stack



2/23/99-added 7

Figure 12

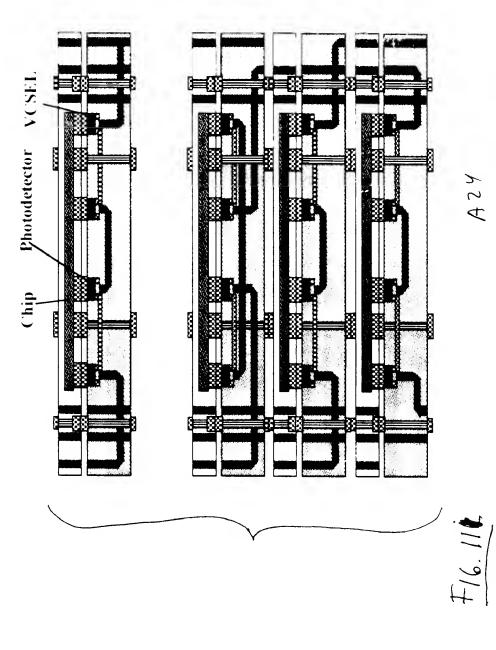
A22 3/7/99



(2/23/99) AA1 Detail picture Example for 3D-stack?

(New version of the AA1 of 2/5/99)

A 23 3/7/99



(2/23/99) AA2 Detail picture Example for 3D-stack

(New version of the AA2 of 2/5/99)

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# Film/Z-Connection Application to OE-Substrate

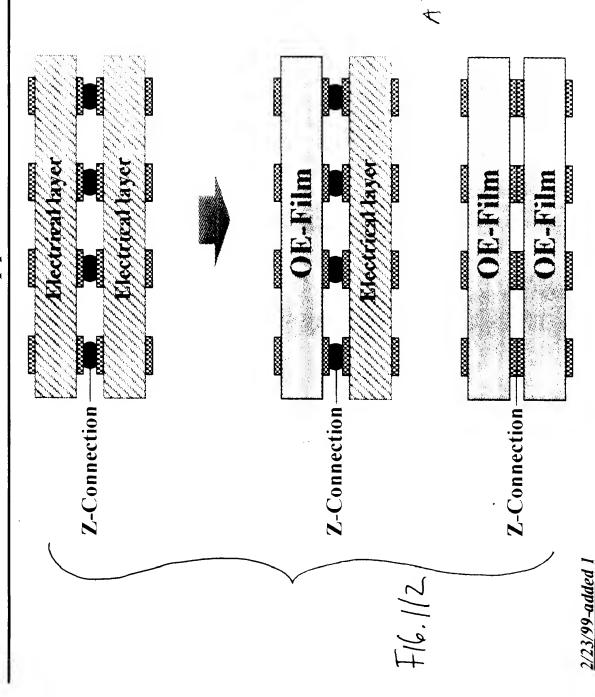
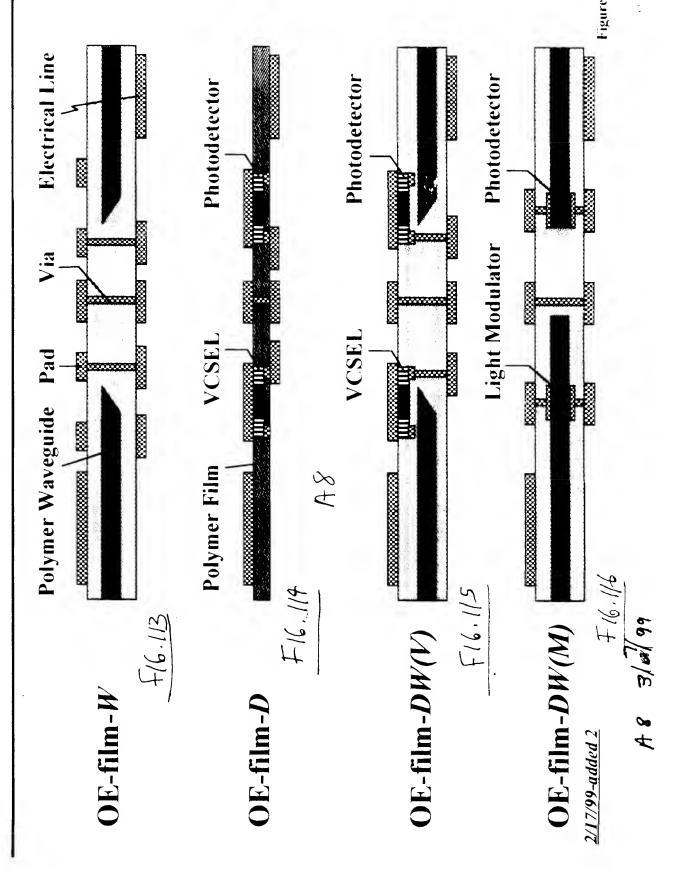
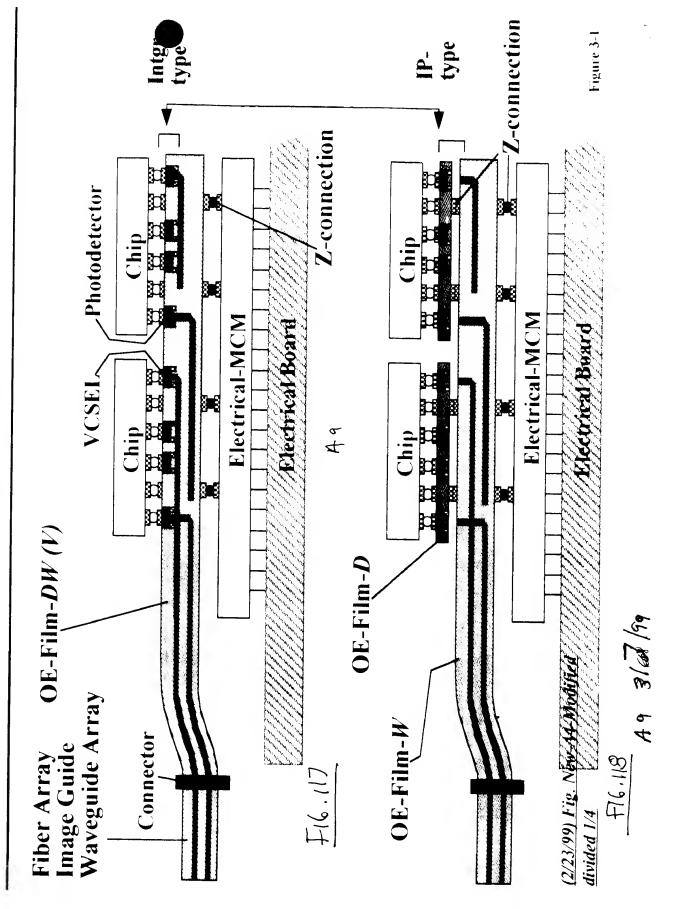


Figure 1

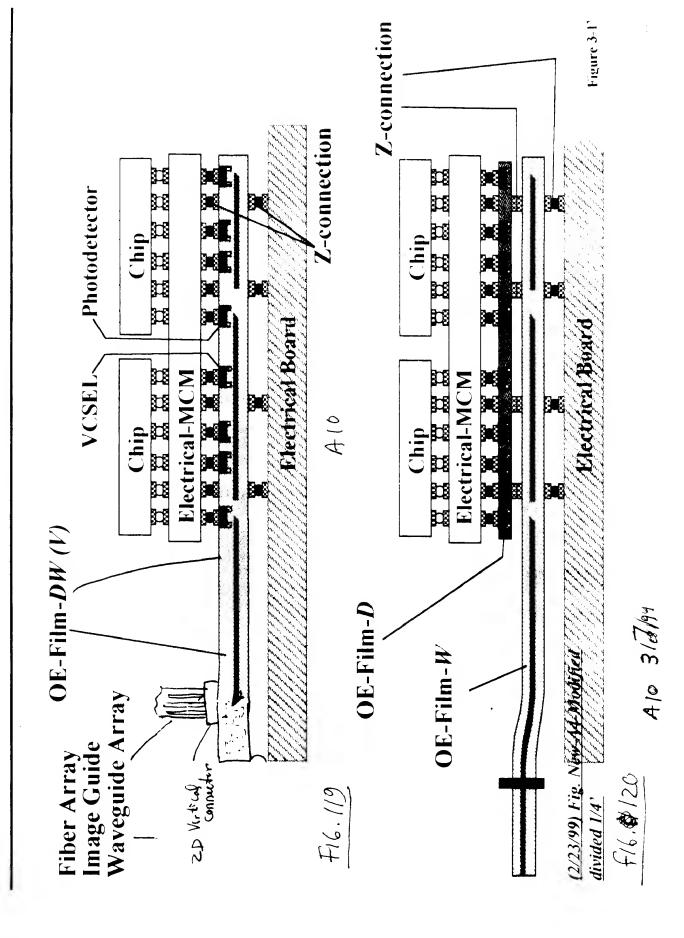
#### **OE-Films**



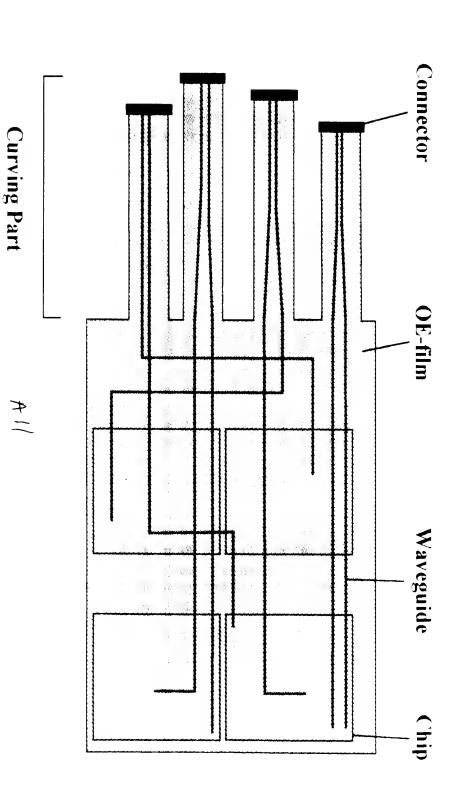
#### **FOLM**



#### FOLM



#### FUJITSU Computer Packaging Technologies, Inc. FOLM with Optical Path Length Controller, Connector Buffer FCPT



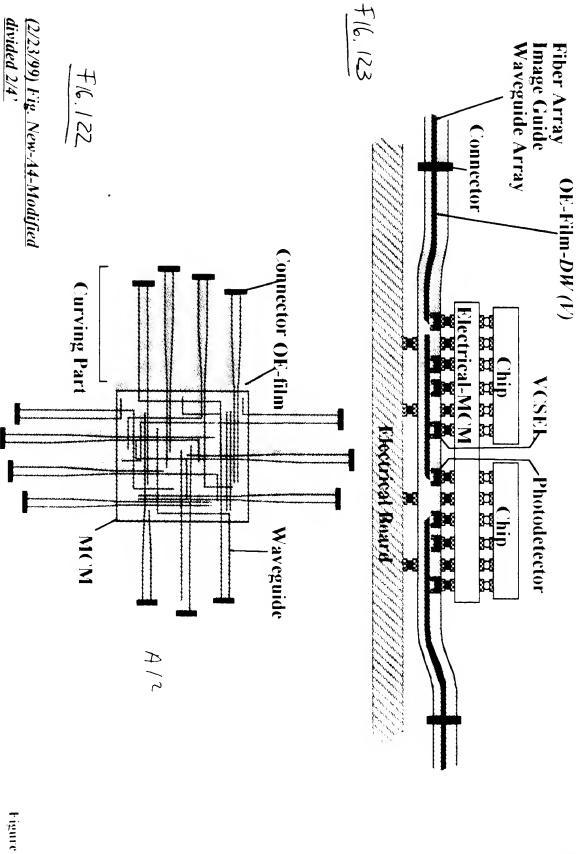
(2/17/99) Fig. New-A4-Modified divided 2/4

A11 3/7/99

F/6. 121

Figure 3-2

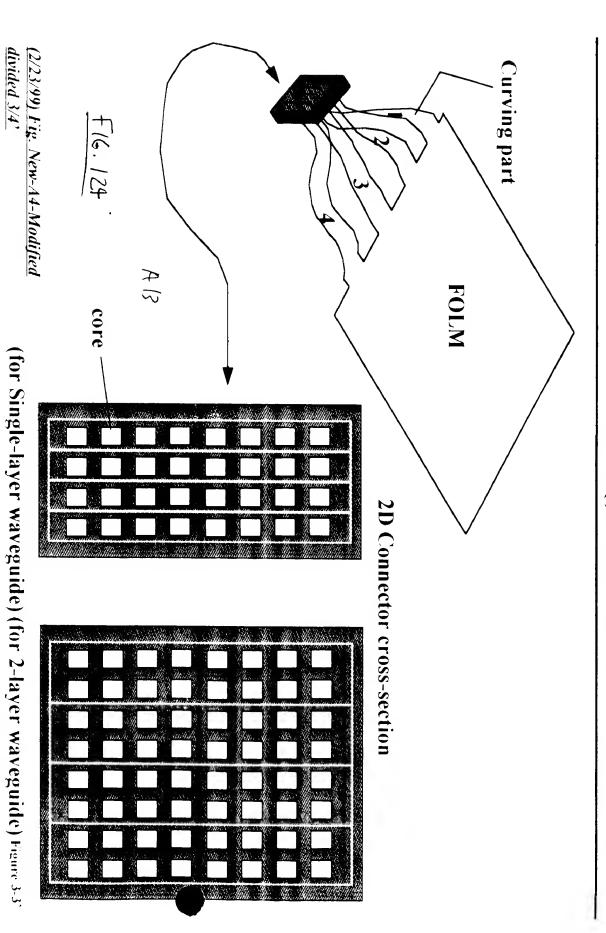
# FOLM with Optical Path Length Controller, Connector Buffer



Alz

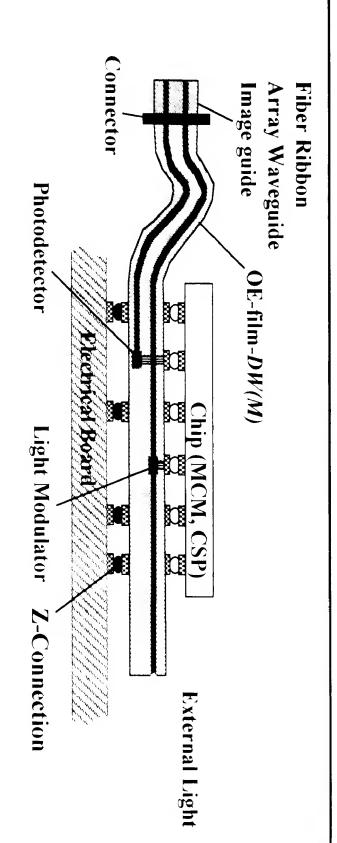
3/7/99

### FOLM with 2D Waveguide Connector



A 183 3/7/99

### FOLM: High-Speed Option

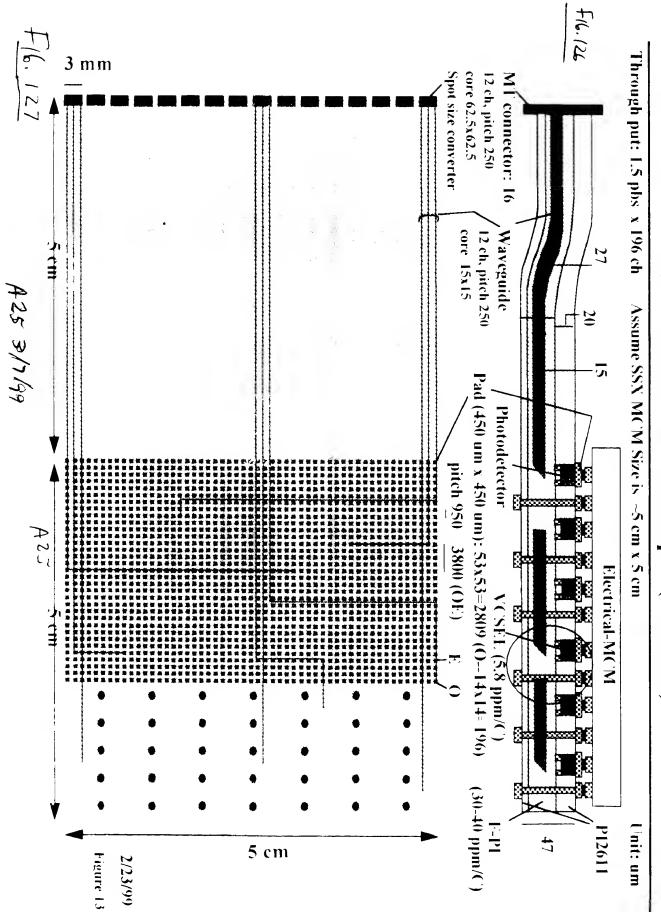


F16.125

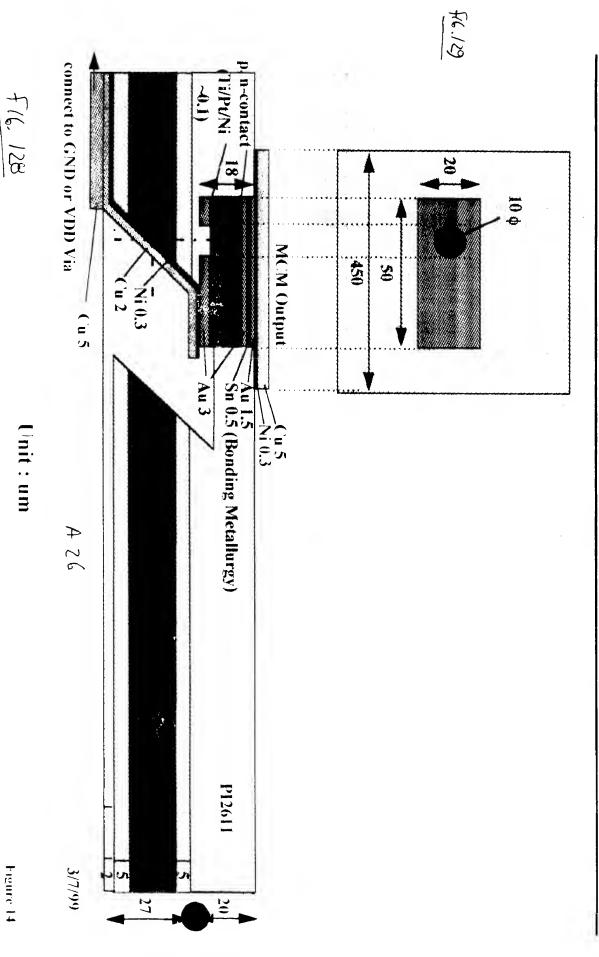
4/4

(2/17/99) Fig. New-A4-Modified divided 4/4

### FOLM Structure Example (Overall)



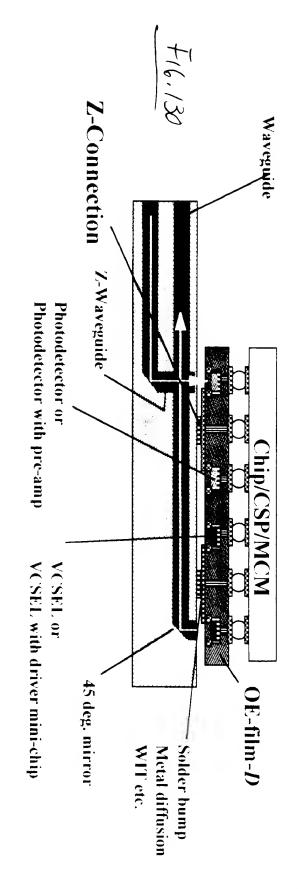
## FOLM Structure Example (VCSEL part)

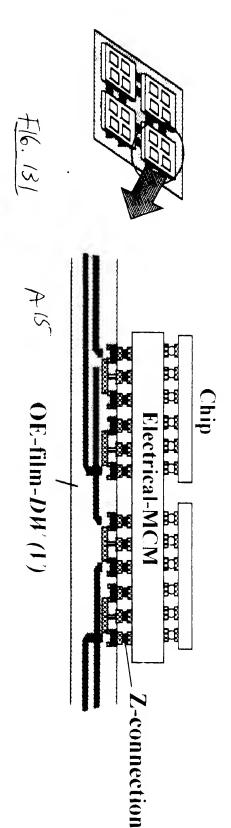


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Figure 14

### OE-film: OE-IP, OE-Film-MCM



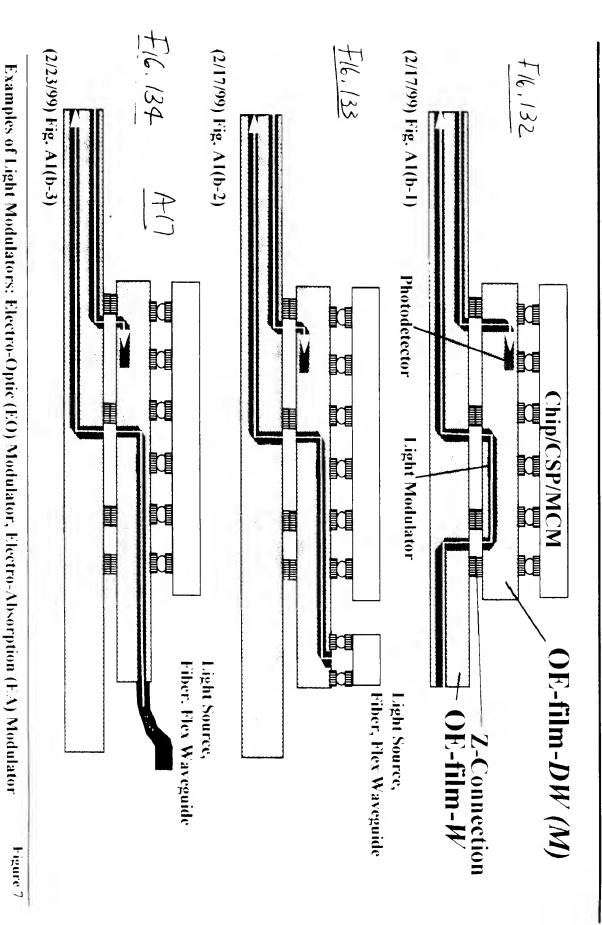


(2/23/99) Fig. New-A1-Modified"

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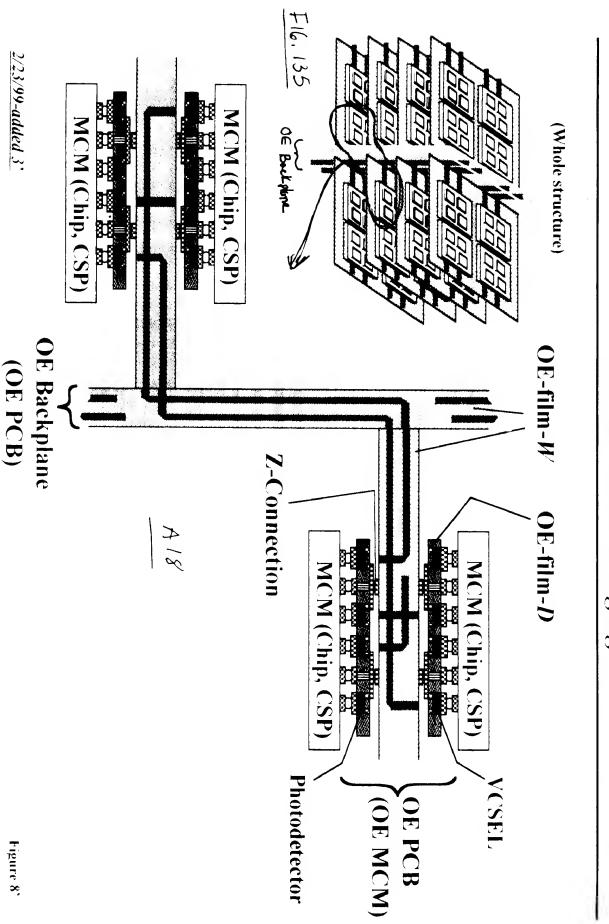
Figure 5

### **OE-film: Light Modulator Transmitters**



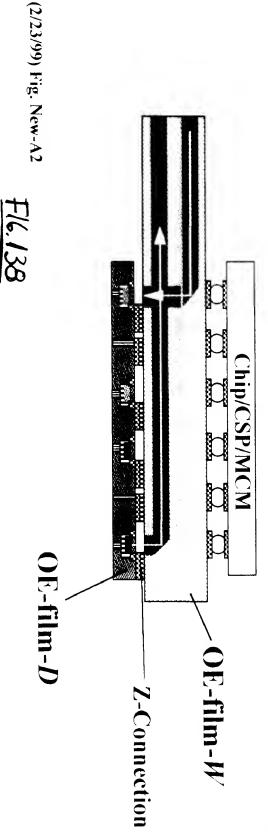
A17 3/7/99 (AL

### OE-film: Both-Side Packaging



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### OE IP is Placed on the Oposit Side

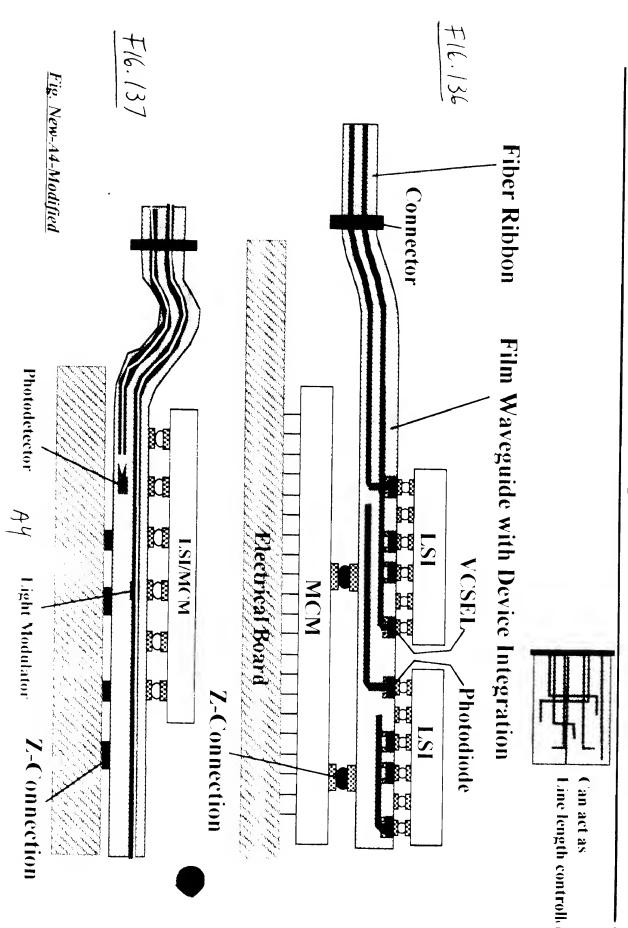


A20

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136.11

#### Direct Jump from LSI



#### OE MCM

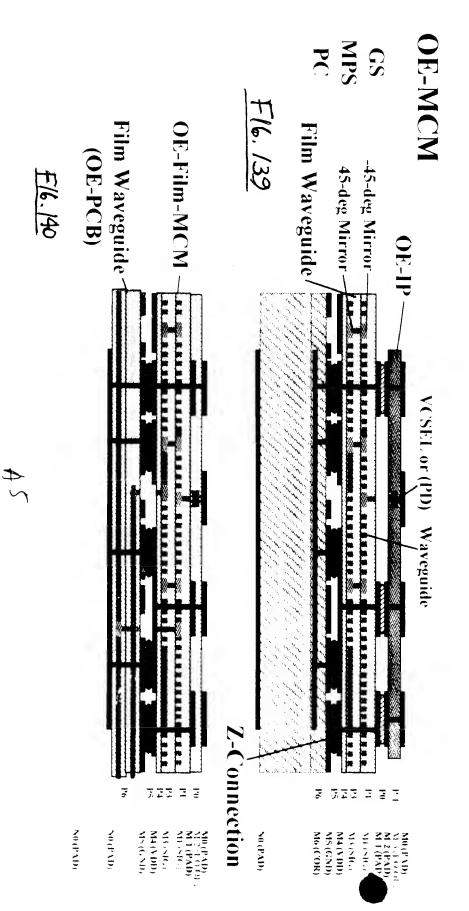
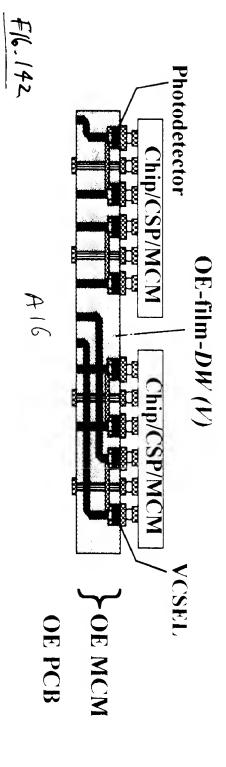


Fig. A5-Modified

#### **OE-film: Smart Pixel**

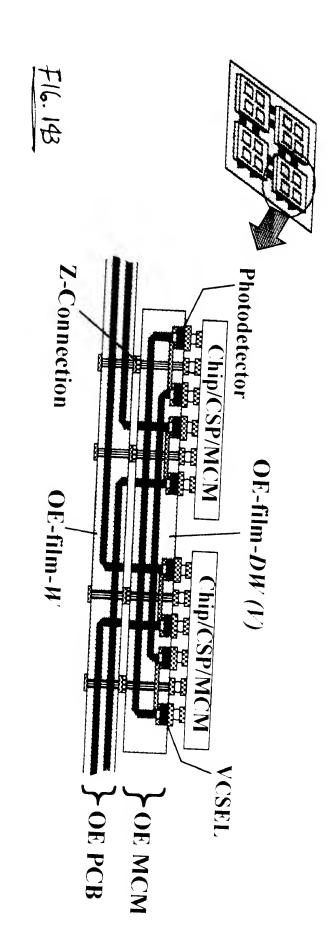


F16. 141



2/23/99-added 6'

# OE-Film/OE-Film Stack --- Back-Side Connection



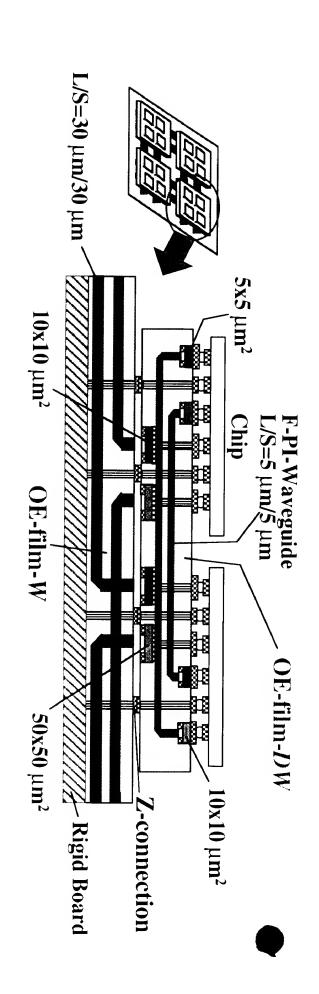
A19

2/23/99-udded 4"

A19 3/7/99

Figure 9°

# OE-Film/OE-Film Stack --- Back-Side Connection



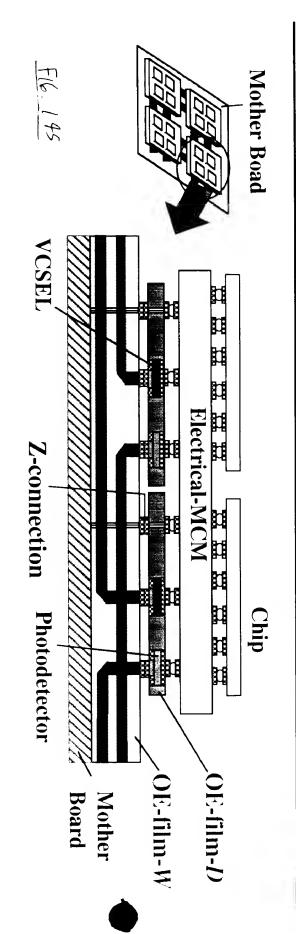
F16. 144

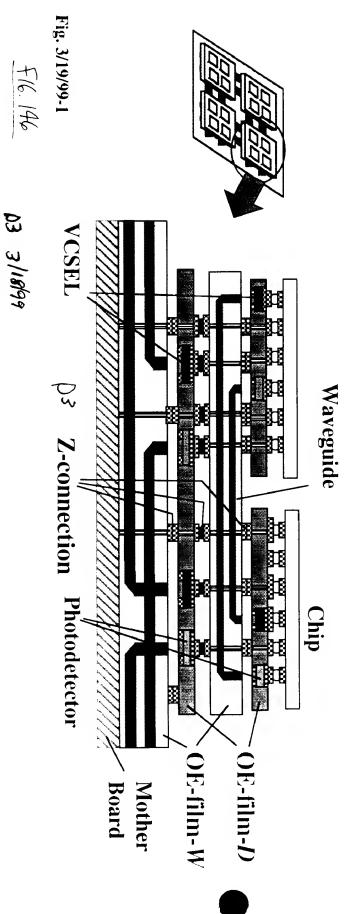
Fig. 3/18/99-1

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\$1 3/18/99

#### **OE-MCM/OE-Bord Stack**





### Device Integration Process

- (1) Pads/Lines formation

F16. 147

(2) Placement of Thin-film devices



(3) Polymer coat



(4) Planarization



(5) Vias/Pads/Lines formation





**→** (6) Substrate removal

A2/

OE-film-D

**▶** (6') Jump to the waveguide formation process



716.15